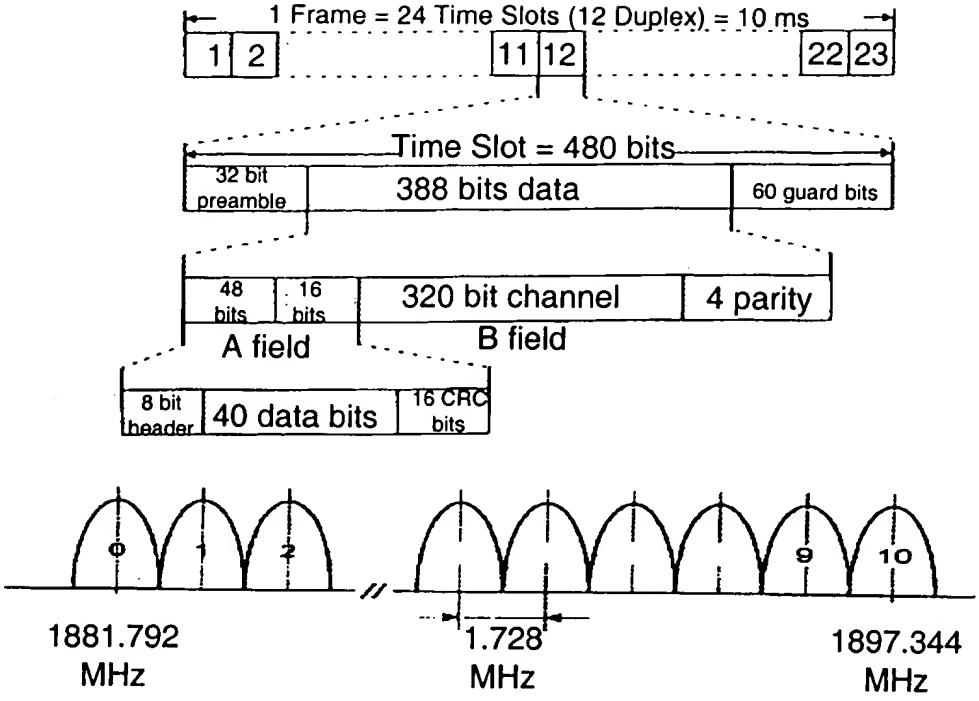

Fig. 1
(Prior Art)

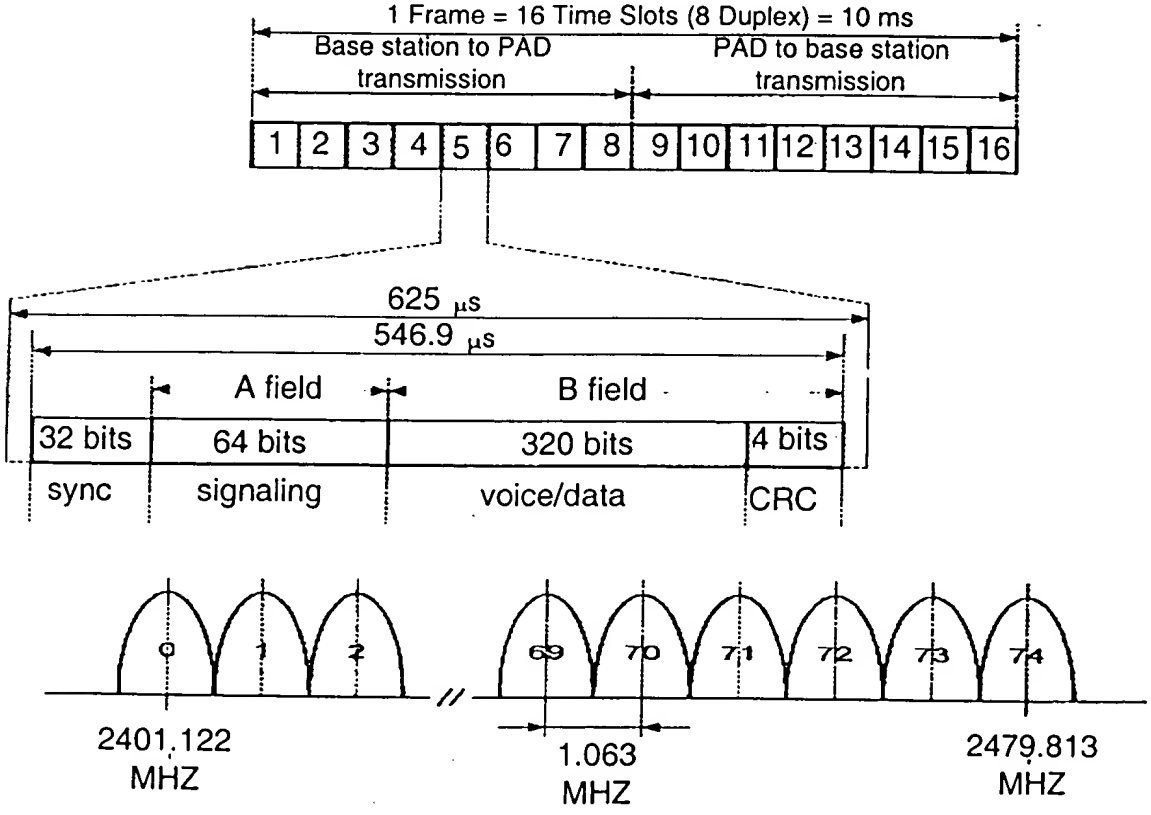

Fig. 7

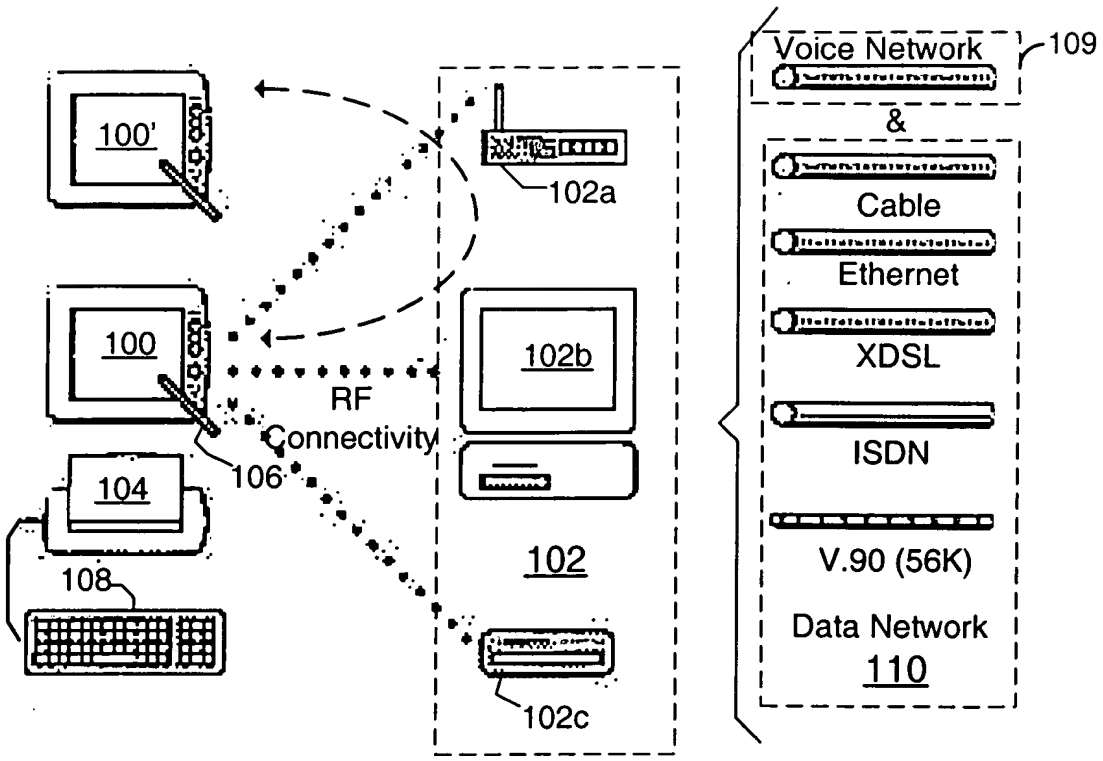
Voice Network
109
&
Cable
Ethernet
XDSL
ISDN
V.90 (56K)
Data Network
110
100'
100
102a
102b
RF
Connectivity
106
104
108
102
102c

Fig. 2

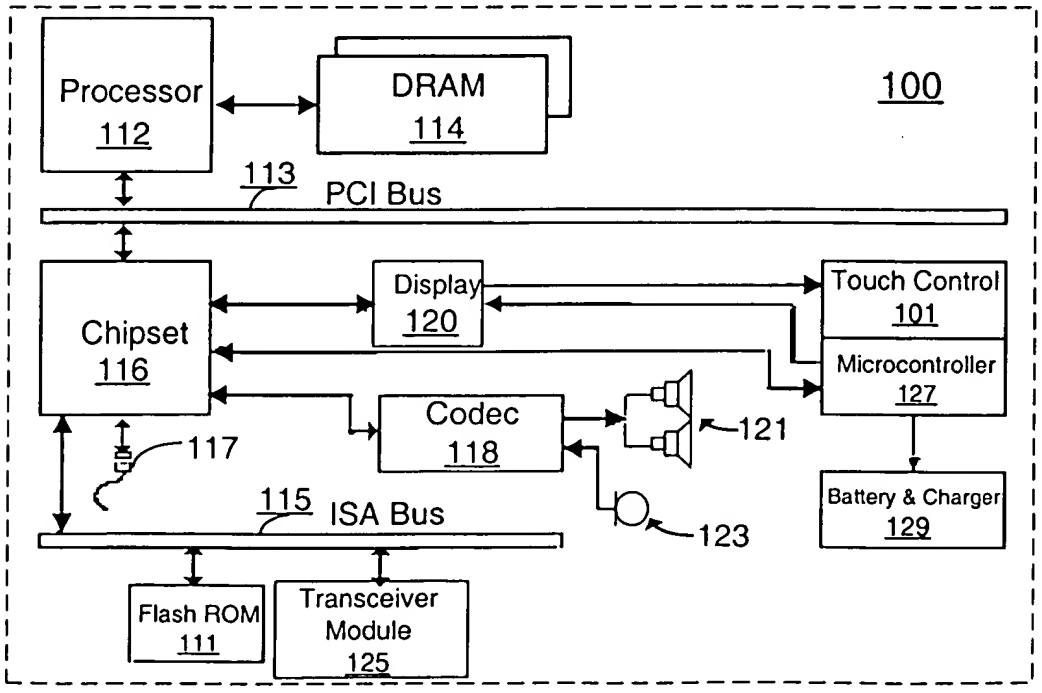
Processor
112
DRAM
114
100
113
PCI Bus
Chipset
116
Display
120
Touch Control
101
Microcontroller
127
Codec
118
121
117
115
ISA Bus
123
Battery & Charger
129
Flash ROM
111
Transceiver
Module
125

Fig. 3

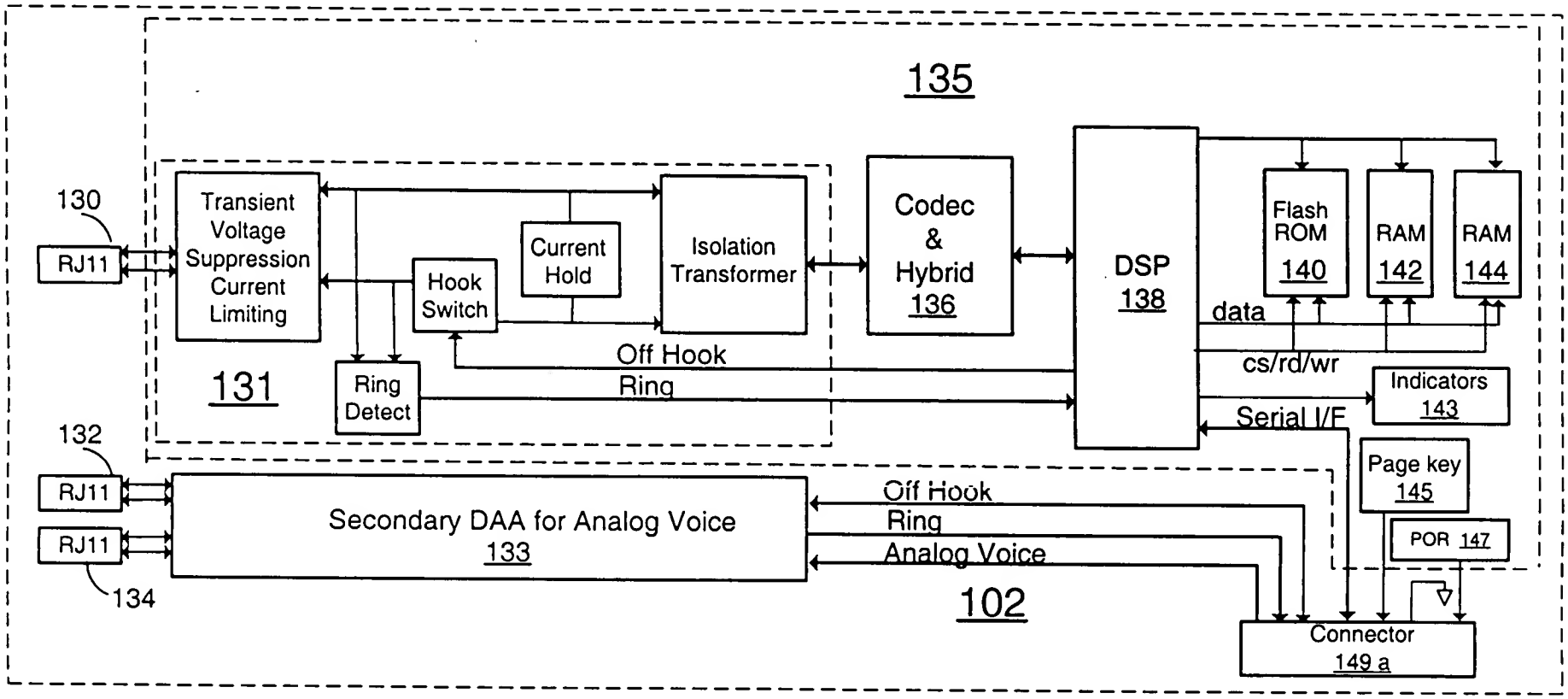
135
130
RJ11
Transient Voltage Suppression Current Limiting
Hook Switch
Current Hold
Isolation Transformer
Codec & Hybrid 136
DSP 138
Flash ROM 140
RAM 142
RAM 144
data
cs/rd/wr
Indicators 143
Serial I/F
Page key 145
POR 147
131
Ring Detect
Off Hook
Ring
132
RJ11
RJ11
134
Secondary DAA for Analog Voice 133
Off Hook
Ring
Analog Voice
102
Connector 149 a

Fig. 4

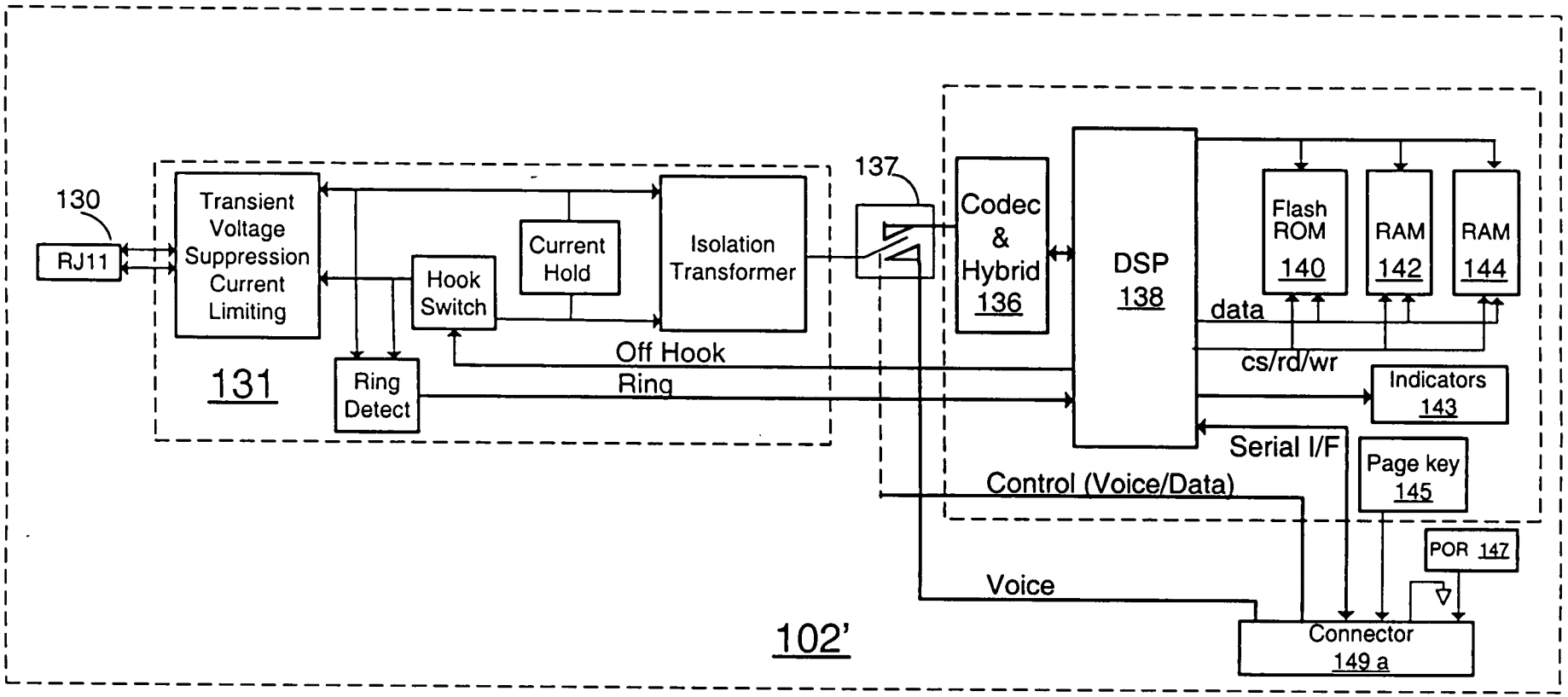
130
RJ11
Transient Voltage Suppression Current Limiting
131
Hook Switch
Current Hold
Isolation Transformer
Ring Detect
Off Hook
Ring
137
Codec & Hybrid 136
DSP 138
Flash ROM 140
RAM 142
RAM 144
data
cs/rd/wr
Indicators 143
Serial I/F
Page key 145
Control (Voice/Data)
POR 147
Voice
Connector 149 a
102'

Fig. 5

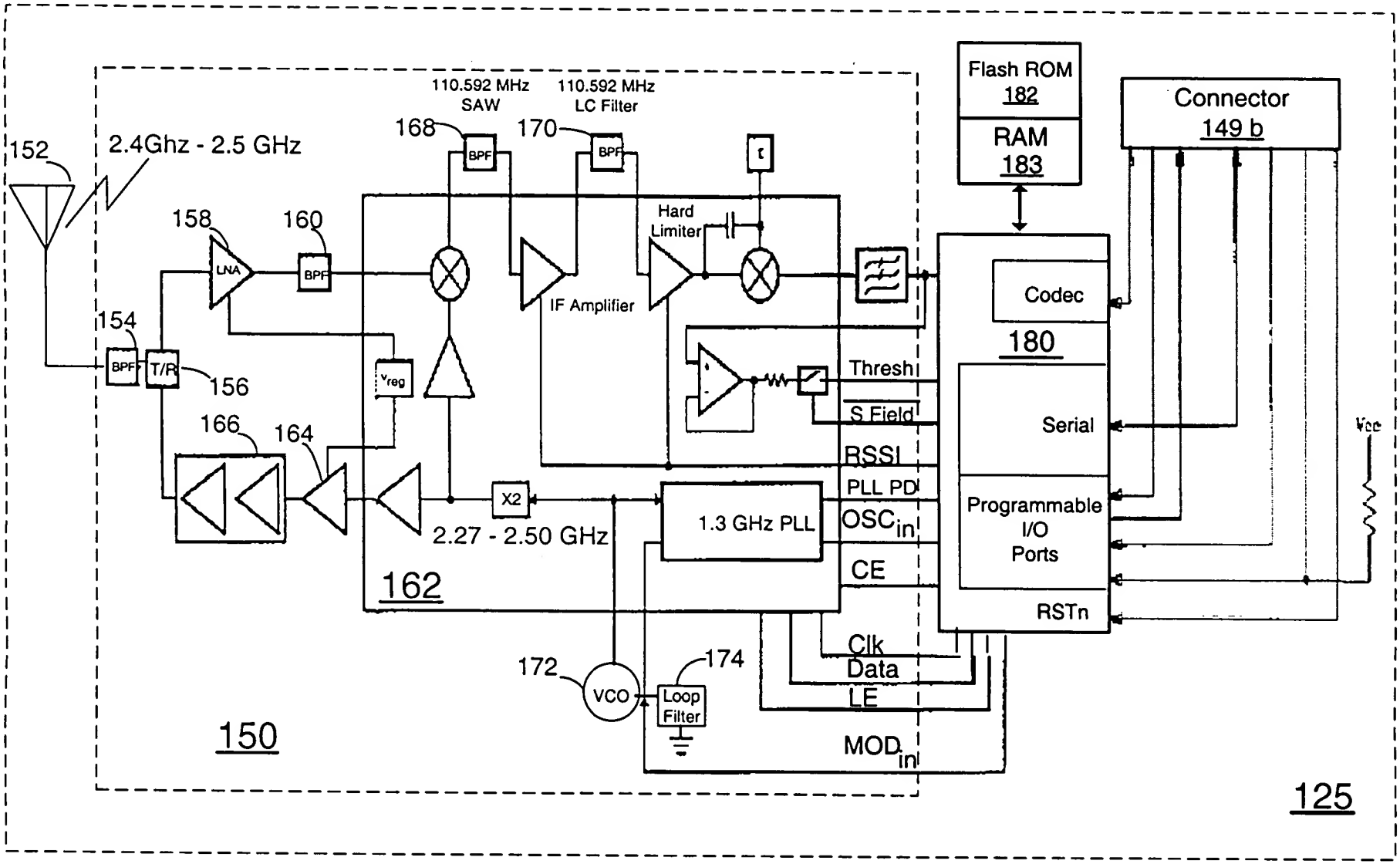
Flash ROM
182
RAM
183
Connector
149 b
110.592 MHz
SAW
110.592 MHz
LC Filter
168
170
152
2.4Ghz - 2.5 GHz
158
160
Hard
Limiter
LNA
BPF
IF Amplifier
154
BPF
T/R
156
Vreg
Codec
180
Thresh
S Field
Serial
RSSI
166
164
PLL PD
X2
1.3 GHz PLL
OSCin
Programmable
I/O
Ports
2.27 - 2.50 GHz
CE
162
RSTn
Clk
172
174
Data
VCO
Loop
Filter
LE
150
MODin
125

Fig. 6

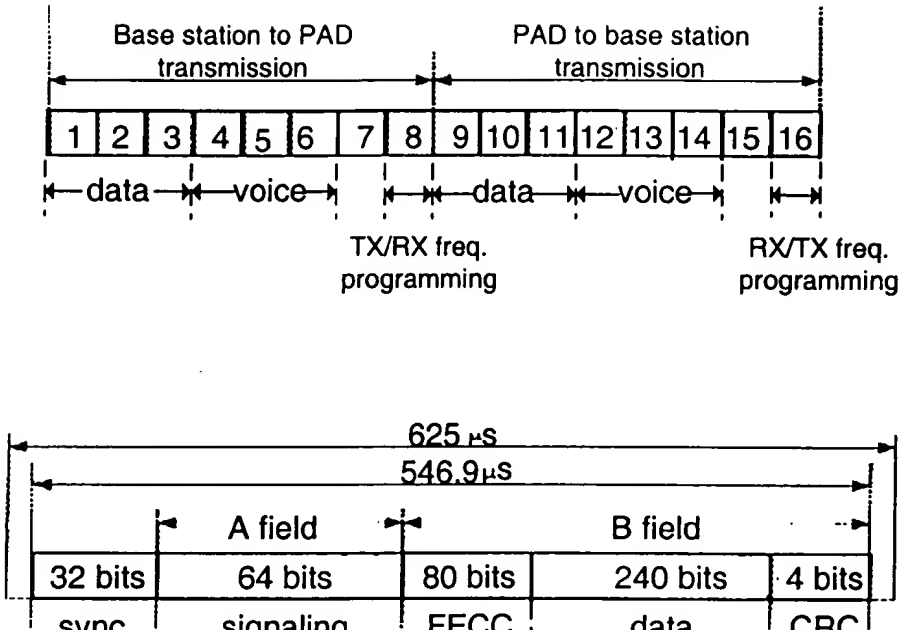

Fig. 8

PREAMBLE		HEADER			DATA
SYNC WORD 80 BITS	FRAME TIMING 16 BITS	DATA WORD LENGTH 12 BITS	DATA RATE 4 BITS	ERROR CHECK WORD 16 BITS	DATA 1-4095 BYTES

128 BITS

Fig. 9
(Prior Art)